

9/10/04

2131
JLW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Application of: Michael I. Catherwood
Serial No.: 09/870,460
Date Filed: June 1, 2001
Group Art Unit: 2131
Examiner: Christopher A. Revak
Title: *Modified Harvard Architecture Processor Having Data Memory Space Mapped To Program Memory Space With Erroneous Execution Protection*

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

Dear Sir:

**REVOCATION OF POWER OF ATTORNEY AND APPOINTMENT
OF NEW ATTORNEYS FOR NON-PROVISIONAL APPLICATION, WITH
CERTIFICATE UNDER 37 CFR 3.73(b)**

Microchip Technology Incorporated, a Delaware corporation, certifies that it is the assignee in the patent application identified above by virtue of a chain of title from the inventor of the patent application identified above. Microchip Technology Incorporated hereby revokes all prior powers of attorney in the subject application and appoints the following as principal attorneys with full power to prosecute this application and transact all business in the United States Patent and Trademark Office connected therewith.

Microchip Technology Incorporated hereby appoints the following:

Practitioners at Customer Number **31625 & 23640**

all of the firm of Baker Botts L.L.P., as attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith, and to file and prosecute any international patent applications filed thereon before any international authorities.

Send Correspondence To:

Customer No. **31625**

Direct Telephone Calls To:

Bruce W. Slayden II
at (512) 322-2606
Atty. Docket No. 068354.1468

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

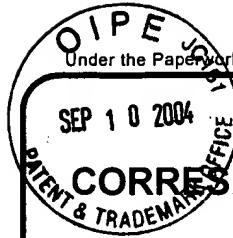
MICROCHIP TECHNOLOGY
INCORPORATED

By: 

Gordon Parnell

Its: Chief Financial Officer

Date: August 17, 2004



SEP 10 2004

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CORRESPONDENCE ADDRESS
Application**

 Address to:
 Assistant Commissioner for Patents
 Washington, D.C. 20231

Application Number	09/870,460
Filing Date	June 1, 2001
First Named Inventor	Michael I. Catherwood
Art Unit	2131
Examiner Name	Christopher A. Revak
Attorney Docket Number	068354.1468

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- Applicant/Inventor.
- Assignee of record of the entire interest.
- Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).
- Attorney or Agent of record.
- Registered practitioner named in the application transmittal letter in an application without an executed oath or declaration. See 37 CFR 1.33(a)(1). Registration Number _____

Typed or Printed Name

Bruce W. Slayden II

Signature

Date

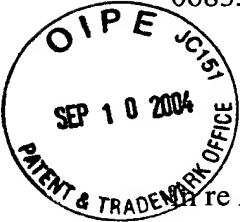
9/10/04

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

 *Total of _____ forms are submitted.

ATTORNEY DOCKET
068354.1468

PATENT APPLICATION
09/870,460



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Attorney Application of:

Michael I. Catherwood

Serial No.:

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*Modified Harvard Architecture Processor Having
Data Memory Space Mapped to Program Memory
Space with Erroneous Execution Protection*

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATE OF MAILING BY EXPRESS MAIL

I hereby certify that the attached Revocation of Power of Attorney and Appointment of New Attorneys and Change of Correspondence Address is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on this 10 day of September, 2004, and is addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Angela Loding

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